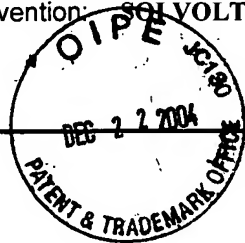


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CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)		Docket No.	
Applicant(s): Steven Voldman		BU999193US2	

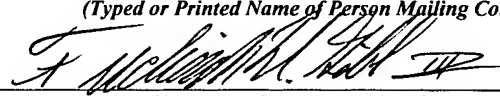
Application No. 10/631,098	Filing Date 07/31/03	Examiner Nguyen, Long T.	Customer No. 29154	Group Art Unit 2816
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Invention: SOL VOLTAGE-TOLERANT BODY-COUPLED PASS TRANSISTOR



I hereby certify that this SUBMISSION OF PROPOSED DRAWING CORRECTIONS
(Identify type of correspondence)

is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on
12/21/04
(Date)

Frederick W. Gibb, III
(Typed or Printed Name of Person Mailing Correspondence)

(Signature of Person Mailing Correspondence)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

patent application of:

Steven H. Voldman

Serial No.: 10/631,098

Filed: July 31, 2003

Group Art Unit: 2816

Examiner: Long T. Nguyen

Atty. Docket No.: BUR919990193US2

For: SOI VOLTAGE-TOLERANT BODY-COUPLED PASS TRANSISTOR

Commissioner of Patents
PO BOX 1450
Alexandria, VA 22313-1450

SUBMISSION OF PROPOSED DRAWING CORRECTIONS

Sir:

Replacement drawing sheets are being submitted simultaneously herewith to correct minor and obvious draftsman errors. More specifically, Figure 5 is corrected to add the connection to Vdd (which is correctly shown in Figure 6). In addition, Figure 6 is corrected to relocate the identification number 45 (which is correctly shown in Figure 5). Applicant submits that these draftsman-related errors are obvious to one ordinarily skilled in the art, and that these corrections do not present new matter. In view of the foregoing, the Examiner is respectfully requested to enter the replacement sheets within the application to allow the invention be properly illustrated.

10/631,098

Approval and acknowledgment of receipt are respectfully requested.

Respectfully Submitted,

 12-21-04

Frederick W. Gibb, III

Reg. No. 37,629

Date:

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